

The diagram illustrates a television receiver system. The signal flow begins with an RF input entering a TUNER (4822). The output of the tuner is an IF signal, which is then processed by an IF amplifier (8003). The IF amplifier's output is split: one path goes through a variable gain amplifier (8015) to an AGC (Automatic Gain Control) block, which provides feedback to the IF amplifier; the other path goes through a series of blocks including a mixer (represented by a circle with an 'X'), a 1/4 wave delay or filter, and another mixer. This path also includes a PFD (Phase-Frequency Detector) and a PLL (Phase-Locked Loop) section with a 1/4 wave delay and a switch. The PLL section is connected to a reference oscillator (8005) and a frequency divider (8007). The output of the PLL section is fed back to the mixer. The final output of the IF path is a video signal, which is then processed by a video amplifier (8011) to produce a VIDEO output. The audio path is derived from the video signal through an audio amplifier (8009) to produce an AUDIO output. The system also includes an RF/IF TAKE OVER input and an AGC output for external control.

FIG. 82

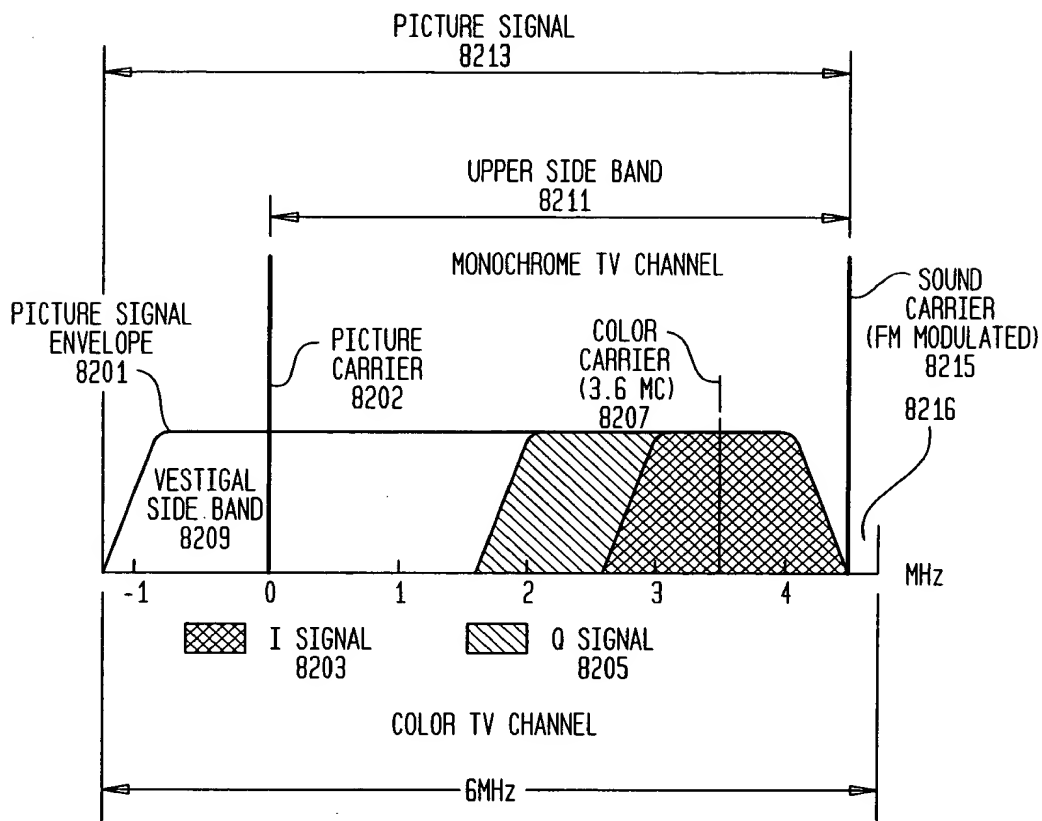


FIG. 83

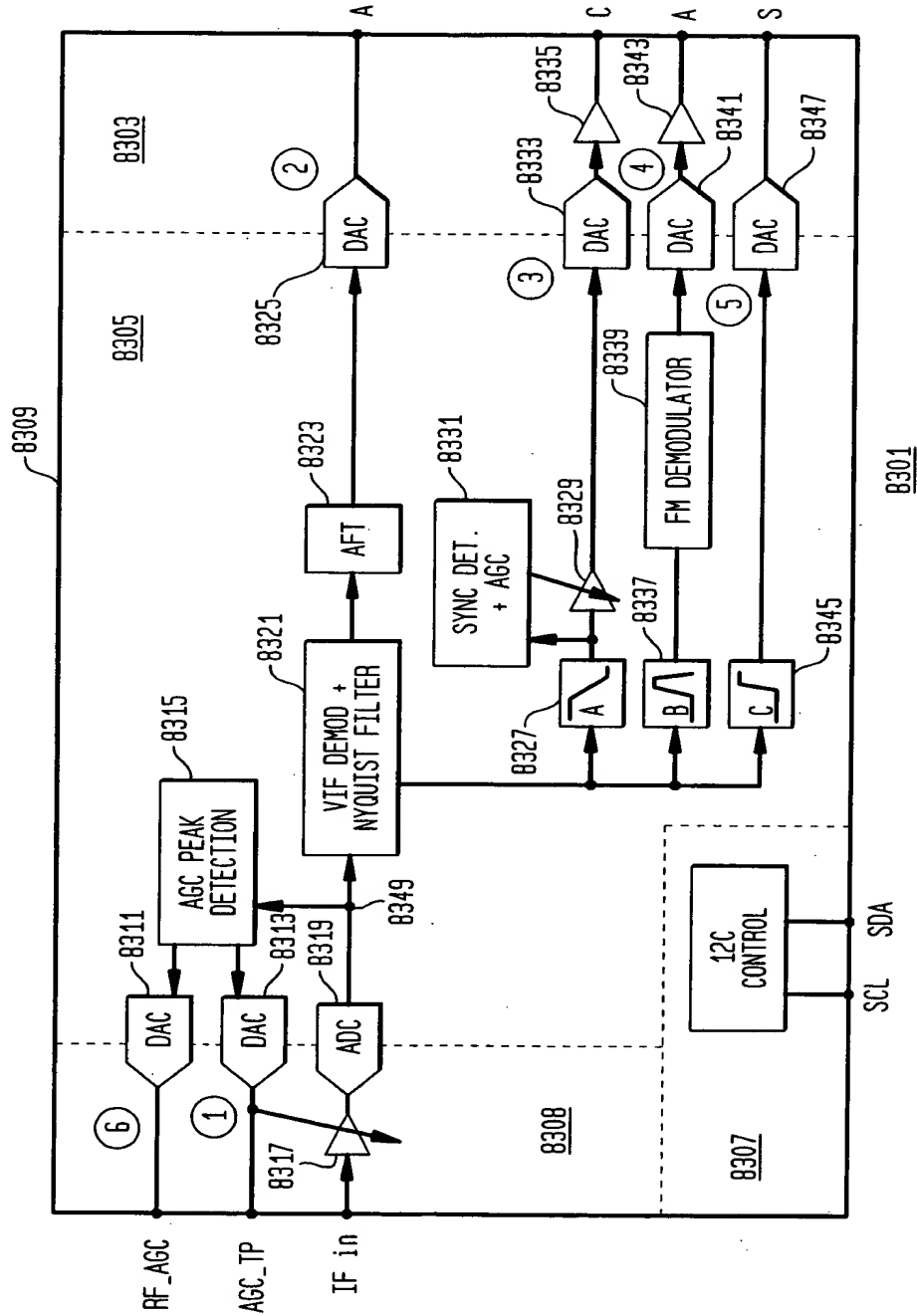


FIG. 84A

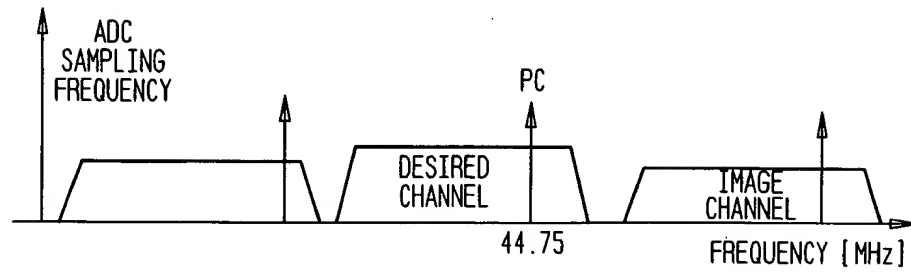


FIG. 84B

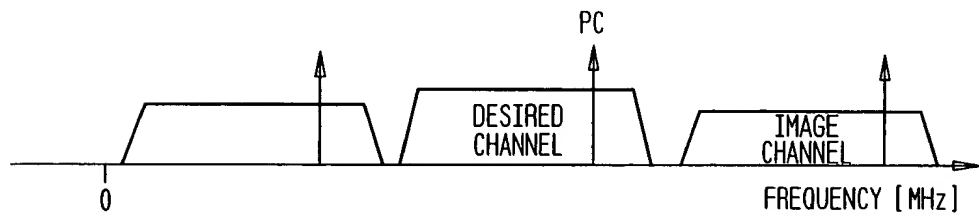


FIG. 84C

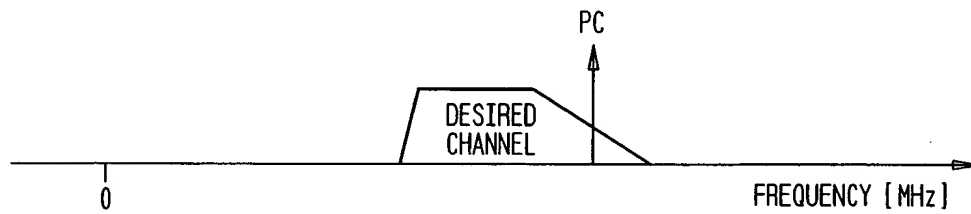


FIG. 84D

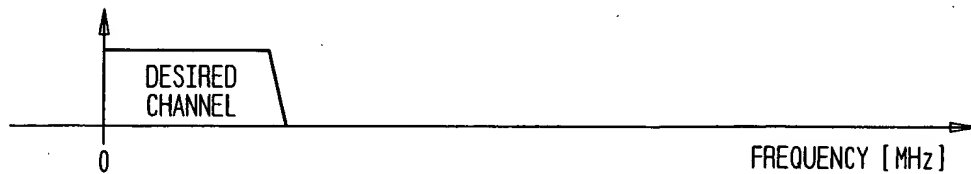


FIG. 85

